



Attorney's Docket No. 1749/261

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re: Stephen Creaney; Richard John Ferguson; Philip Lane; Thomas Crummey
Appl No.: 09/437,004
Filed: November 9, 1999
For: "Data Communications Link" Group Art Unit 2734

Assistant Commissioner for Patents
Washington, DC 20231

January 13, 2000

SUBMITTAL OF FORMAL DRAWINGS

Sir:

Enclosed herewith is one set (six sheets) of formal drawings. It is requested that these drawings be substituted for the originally filed informal drawings.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Box Missing Parts, Assistant Commissioner for Patents, Washington, DC 20231, on January 13, 2000.

Samuel G. Layton, Jr.

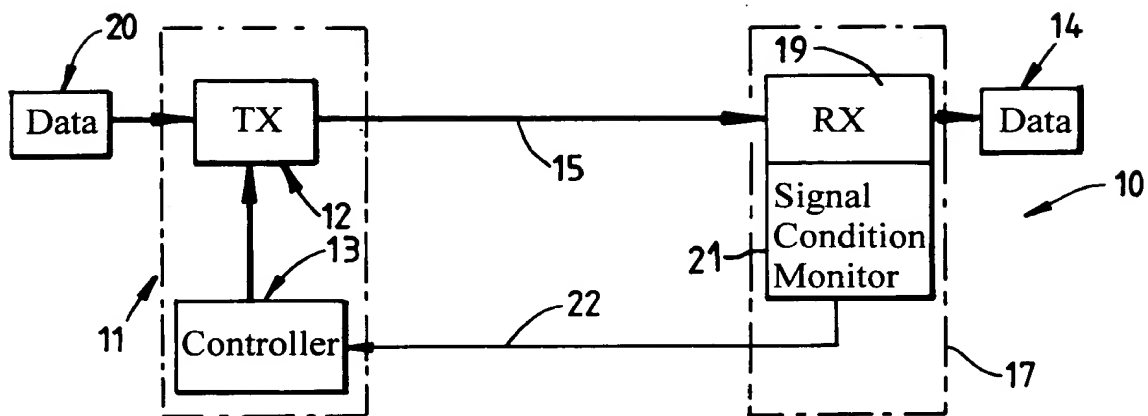


Fig. 1

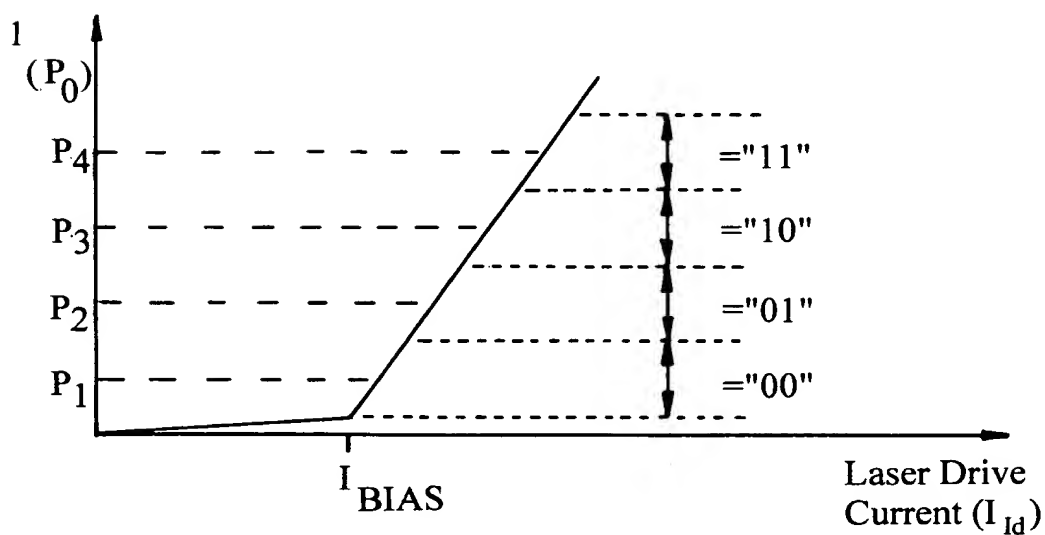
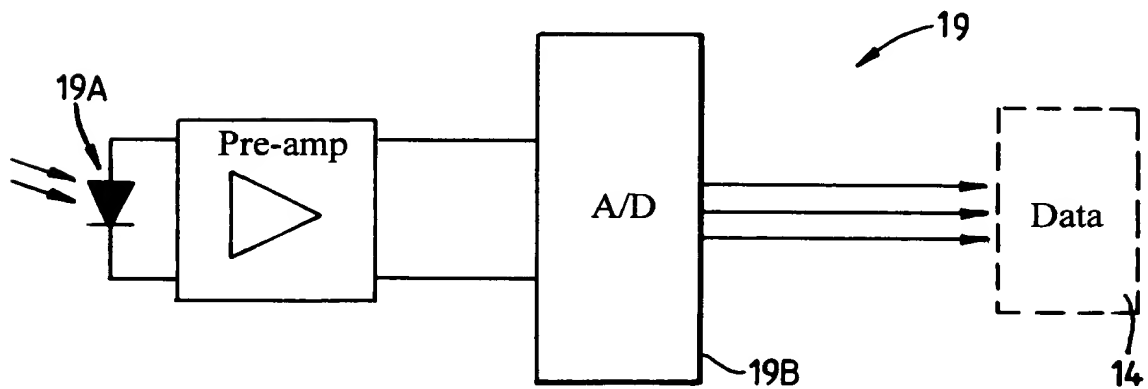
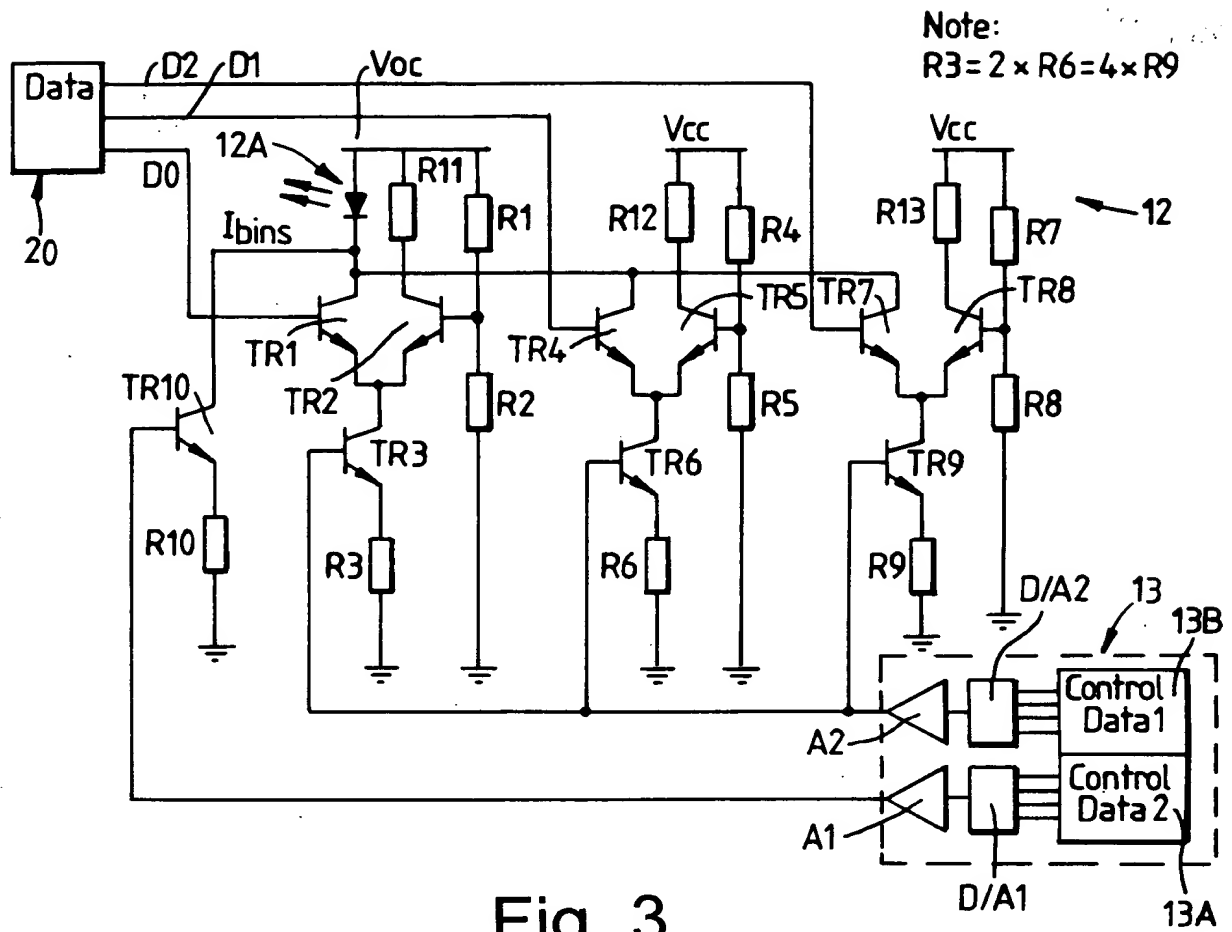


Fig. 2



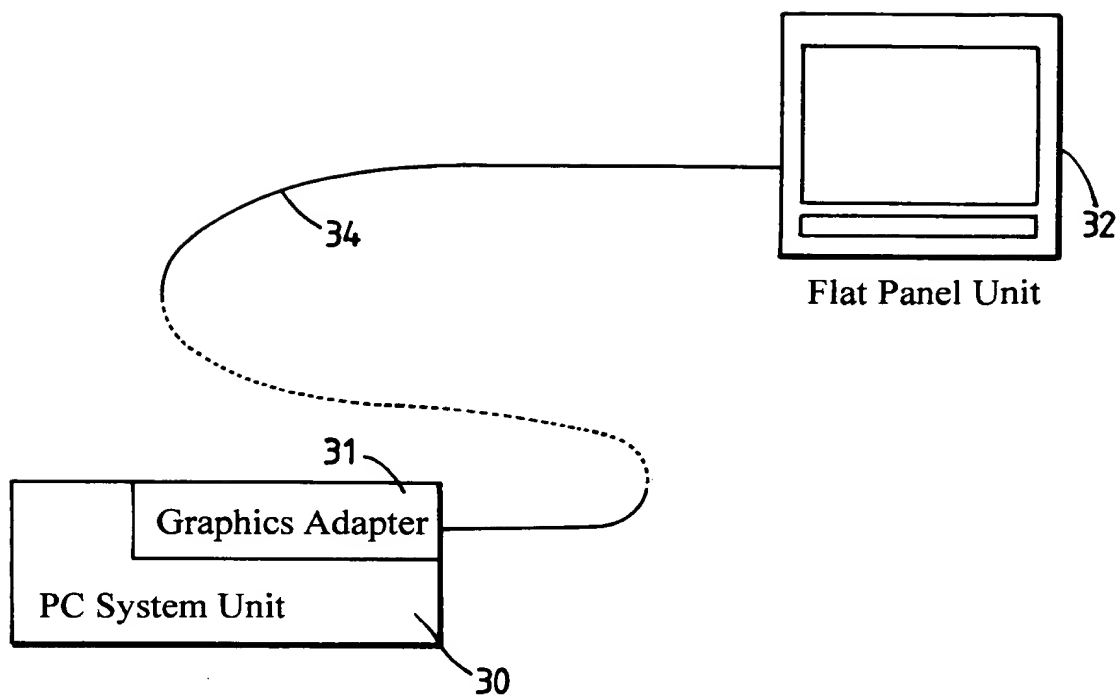


Fig. 5

ALPHA IS MULTI-LEVEL Tx / BINARY Rx, BETA IS MULTI-LEVEL Rx / BINARY Tx
UP/*DOWN_MIN IS A CONTROL SIGNAL USED TO INCREASE OR DECREASE THE
ALPHA LASER BIAS DRIVE CURRENT

INITIAL SIGNAL STATES: UP/*DOWN_MIN='1' TO '0' (INITIATES MIN_CAL_ALPHA)

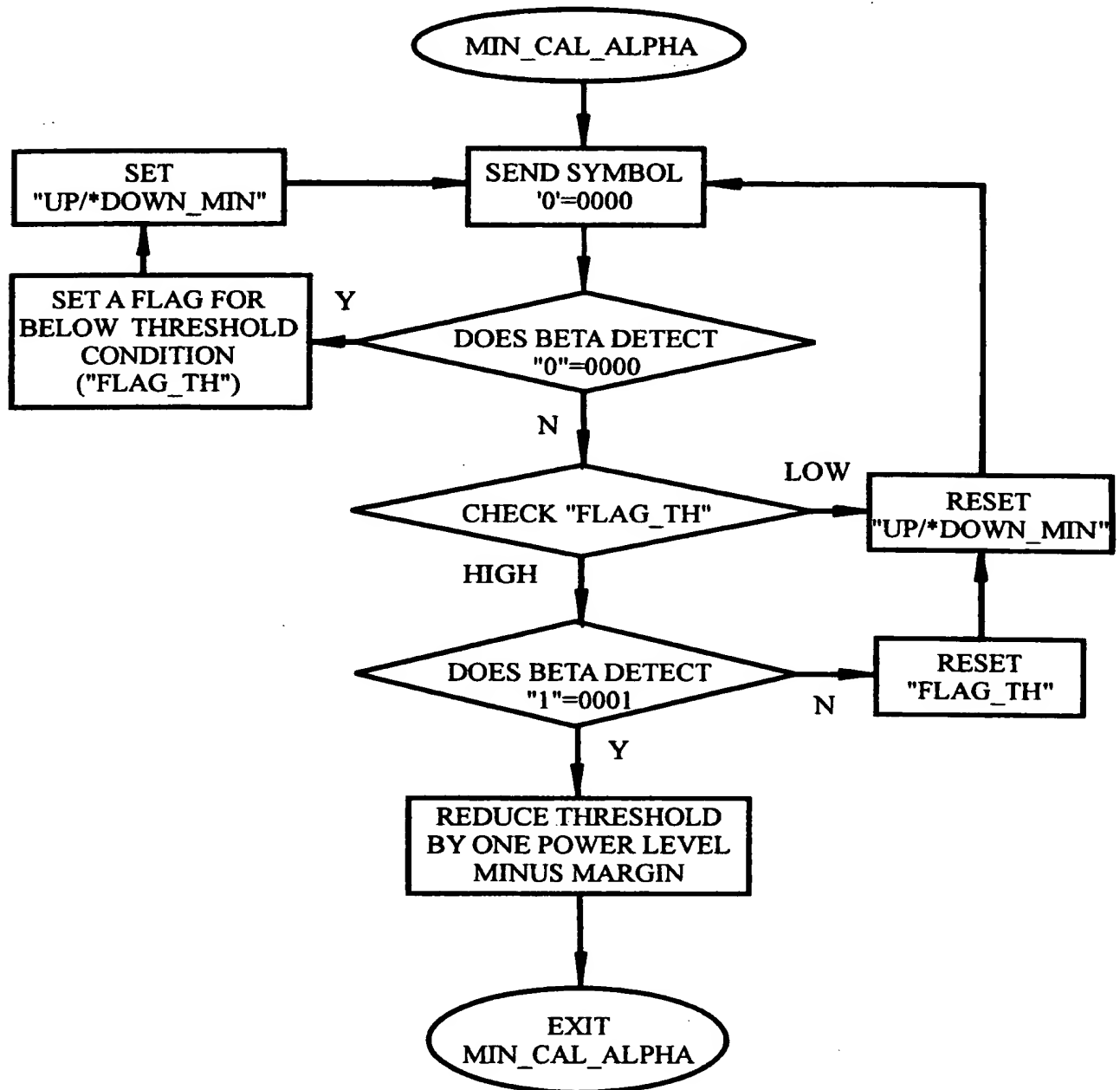


Fig. 6A

ALPHA IS MULTI-LEVEL Tx / BINARY Rx, BETA IS MULTI-LEVEL Rx / BINARY Tx
UP/*DOWN_MAX IS A CONTROL SIGNAL USED TO INCREASE OR DECREASE THE ALPHA
LASER MODULATION DRIVE CURRENT

INITIAL SIGNAL STATES: UP/*DOWN_MAX='0' TO '1' (INITIATES MAX_CAL_ALPHA)

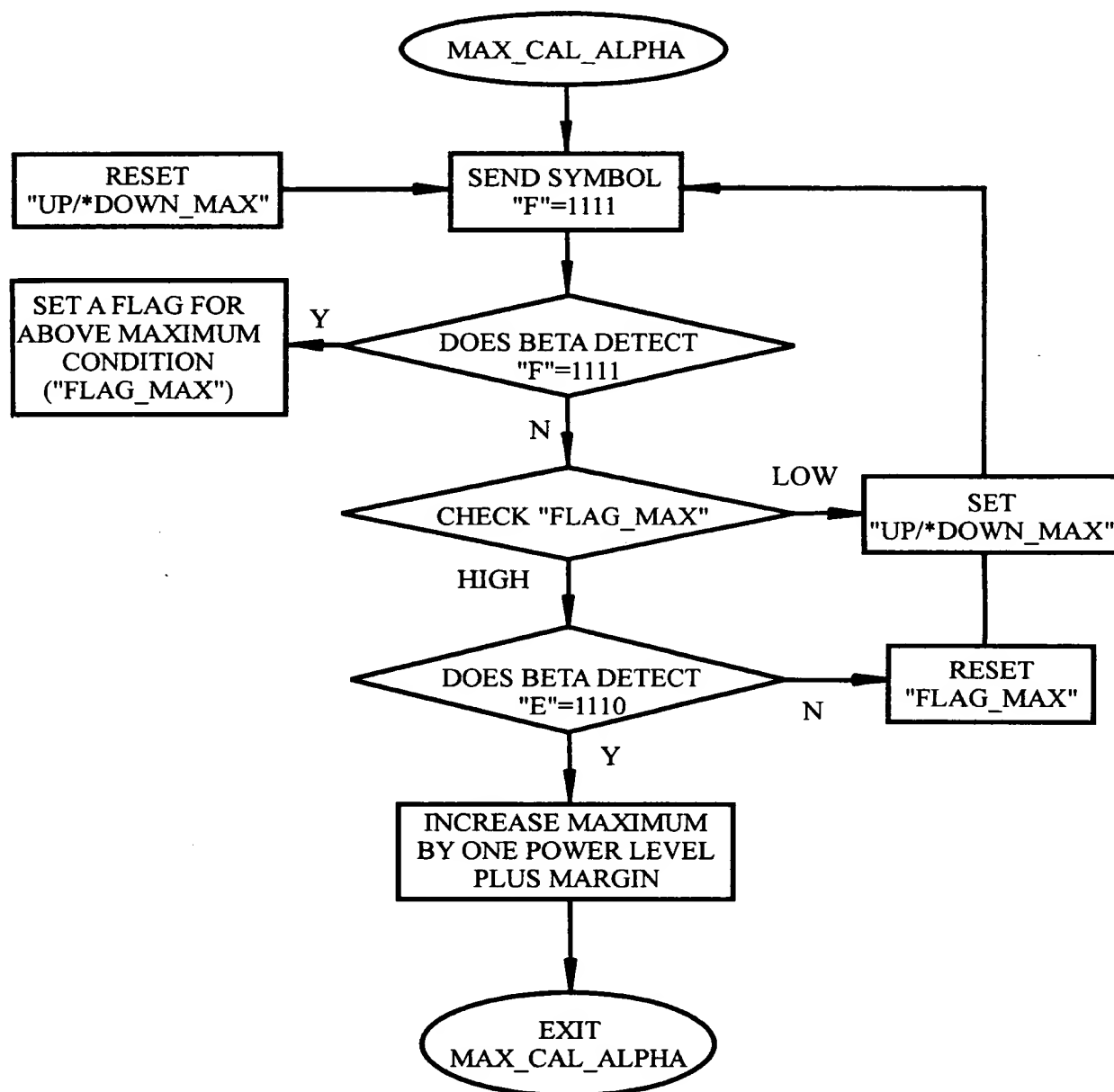


Fig. 6B

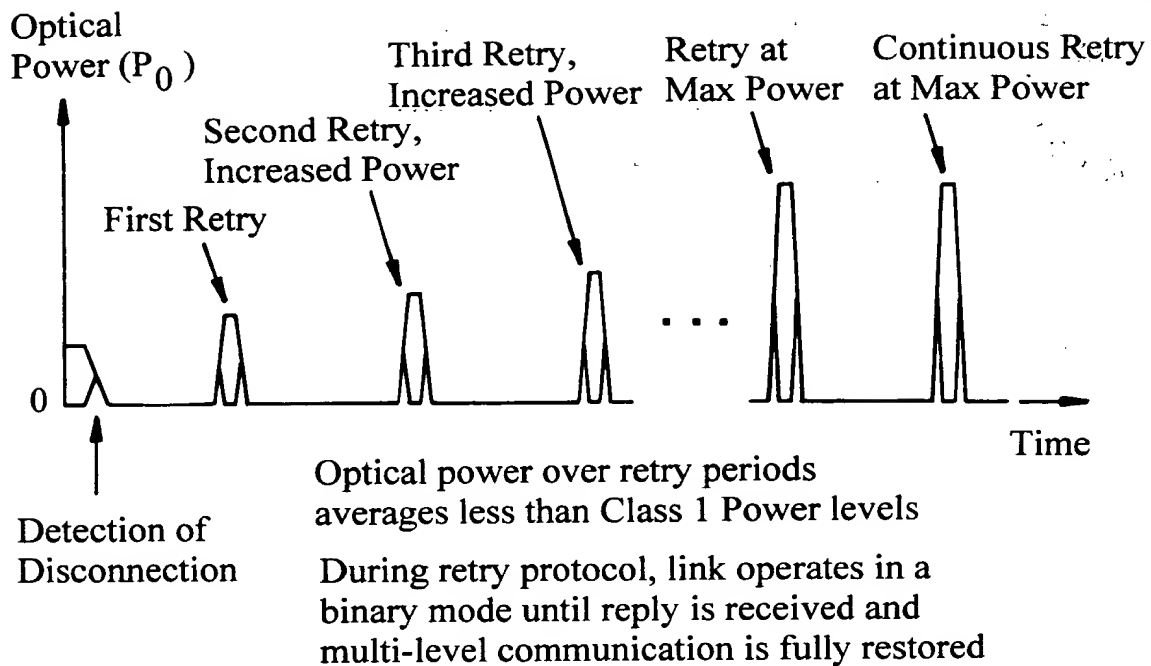


Fig. 7

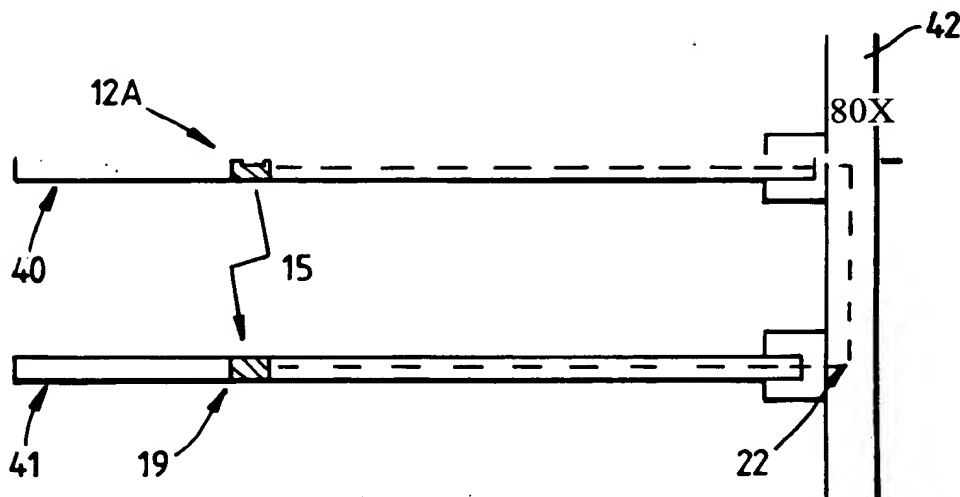


Fig. 8